

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani et al.  
Serial No.: 08/690,747  
Filed : 8/1/96  
Title : METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE

Art Unit: 1109  
Examiner: E. Defillo

Assistant Commissioner for Patents  
Washington, DC 20231

AMENDMENT

Sir:

In response to the Official Action dated February 13,  
1998, paper no. 3 in the above-referenced case, kindly amend the  
above-referenced application as follows:

In the Specification

Page 4, line 3, after "time" insert --in appropriate  
time units--.

Page 4, line 6, after "parenthesis" insert --in  
degrees--.

Date of Deposit

7-13-98  
I hereby certify under 37 CFR 1.8(a) that this correspondence is  
being deposited with the United States Postal Service as first class  
mail with sufficient postage on the date indicated above and is  
addressed to the Assistant Commissioner for Patents, Washington,  
D.C. 20231.

Sanet Chastky  
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